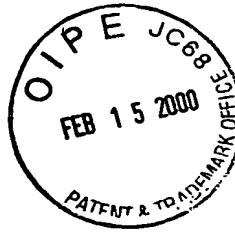


IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: KAJIHARA et al.

Serial No.: 09/328,910

Filed: June 9, 1999



For: LEADFRAME SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE
USING THE SAME, AND METHOD OF AND PROCESS FOR
FABRICATING THE TWO

Group: 2811

Examiner: Unassigned

Attention: Application Processing Division
Special Processing and Correspondence Branch

SUBMISSION OF REISSUE DECLARATION

Assistant Commissioner
for Patents
Washington, D.C. 20231

February 15, 2000

Sir:

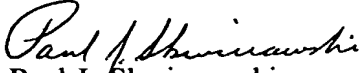
The Notice to File Missing Parts of Application--Filing Date Granted and Reissue Supplement to Notice to File Missing Parts of Application dated July 15, 1999, in connection with the above-identified application is acknowledged. A copy both Notices are attached hereto.

Attached hereto is a declaration in compliance with 37 CFR §1.63 and a Certificate under 37 CFR §3.73 with a check including the amount of \$130.00 in payment of the surcharge set forth in 37 CFR §1.16(e) as required in the Notice.

To the extent necessary, Applicant petitions for an extension of time under 37 CFR §1.136. Please charge any shortage in fees due in connection with the filing of this paper,

including extension of time fees, to the deposit account of Antonelli, Terry, Stout & Kraus, LLP, Deposit Account No. 01-2135 (501.32049R00), and please credit any excess fees thereto.

Respectfully submitted,

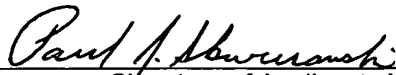

Paul J. Skwierawski

Registration No. 32,173

ANTONELLI, TERRY, STOUT & KRAUS, LLP

PJS/pay
(703) 312-6600

Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it displays a valid OMB control number.

REISSUE APPLICATION FEE TRANSMITTAL FORM						Docket Number (Optional)		
						500.32049R00		
Claims as Filed - Part 1								
Claims in Patent	For	Number Filed in Reissue Application	(3) Number Extra	Small Entity		Other than a Small Entity		
				Rate	Fee	Rate	Fee	
(A) 14	Total Claims (37 CFR 1.16(j))	(B) 62	**** 42 =	x \$	=	or	x \$ 18 = 756.00	
(C) 4	Independent Claims (37 CFR 1.16(i))	(D) 14	* 10 =	x \$	=		x \$ 78 = 780.00	
Basic Fee (37 CFR 1.16(h))					\$		\$ 760.00	
Total Filing Fee					\$	OR	\$296.00	
Claims as Amended - Part 2								
	(1) Claims Remaining After Amendment		(2) Highest Number Previously Paid For	(3) Extra Claims Present	Small Entity		Other than a Small Entity	
					Rate	Fee	Rate	Fee
Total Claims (37 CFR 1.16(j))	***	MINUS	**	=	x \$	=	or	x \$ =
Independent Claims (37 CFR 1.16(i))	***	MINUS	*****	=	x \$	=		x \$ =
Total Additional Fee					\$	OR	\$	
<p>* If the entry in (D) is less than the entry in (C), Write "0" in column 3.</p> <p>** If the "Highest Number of Total Claims Previously Paid For" is less than 20, Write "20" in this space.</p> <p>*** After any cancellation of claims</p> <p>**** If "A" is greater than 20, use (B - A); if "A" is 20 or less, use (B - 20).</p> <p>***** "Highest Number of Independent Claims Previously Paid For" or Number of Independent Claims in Patent (C).</p>								
<p><input type="checkbox"/> Please charge Deposit Account No. _____ in the amount of _____. A duplicate copy of this sheet is enclosed.</p> <p><input checked="" type="checkbox"/> The Commissioner is hereby authorized to charge any additional fees under 37 CFR 1.16 or 1.17 which may be required, or credit any overpayment to Deposit Account No. <u>01-2135</u>. A duplicate copy of this sheet is enclosed.</p> <p><input checked="" type="checkbox"/> A check in the amount of \$ <u>2296.00</u> to cover the filing / additional fee is enclosed.</p>								
June 9, 1999 Date		 Signature of Applicant, Attorney or Agent of Record						
		Paul J. Skwierawski, Reg. No. 32,173 Typed or printed name						

Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it displays a valid OMB control number.

REISSUE APPLICATION DECLARATION BY THE INVENTOR

Docket Number (Optional)

501.32049R00

As a below named inventor, I hereby declare that:

My residence, post office address and citizenship are stated below next to my name.

I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is described and claimed in patent number 5,637,913, granted June 10, 1997, and for which a reissue patent is sought on the invention entitled Leadframe Semiconductor Integrated Circuit Device Using The Same, And Method Of And Process For Fabricating The Two, the specification of which

☐ is attached hereto.

☒ was filed on June 9, 1999 as reissue application number 09 / 328,910 and was amended on _____ (If applicable)

I have reviewed and understand the contents of the above identified specification, including the claims, as amended by any amendment referred to above.

I acknowledge the duty to disclose information which is material to patentability as defined in 37 CFR 1.56.

I verily believe the original patent to be wholly or partly inoperative or invalid, for the reasons described below. (Check all boxes that apply.)

☐ by reason of a defective specification or drawing.

☒ by reason of the patentee claiming more or less than he had the right to claim in the patent.

☐ by reason of other errors.

At least one error upon which reissue is based is described as follows:

Patentee claimed less than he had the right to claim as evidenced by the patented claims 1-14 being each directed to a "semiconductor integrated circuit device", whereas any of added claims 15-36 correct such oversight and claim more by each being alternatively directed to a "method of manufacturing a semiconductor device."

[Page 1 of 2]

Burden Hour Statement: This form is estimated to take 0.5 hours to complete. Time will vary depending upon the needs of the individual case. Any comments on the amount of time you are required to complete this form should be sent to the Chief Information Officer, Patent and Trademark Office, Washington, DC 20231. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Assistant Commissioner for Patents, Washington, DC 20231.

Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it displays a valid OMB control number.

(REISSUE APPLICATION DECLARATION BY THE INVENTOR, page 2)

Docket Number (Optional)

501.32049R00

All errors corrected in this reissue application arose without any deceptive intention on the part of the applicant. As a named inventor, I hereby appoint the following attorney(s) and/or agent(s) to prosecute this application and transact all business in the Patent and Trademark Office connected therewith.

Donald R. Antonelli, Reg. No. 20,296; David T. Terry, Reg. No. 20,178; Melvin Kraus, Reg. No. 22,266; William I. Solomon, Reg. No. 28,565; Gregory E. Montone, Reg. No. 28,146; Ronald J. Shore, Reg. No. 28,577; Donald E. Stout, Reg. No. 26,422; Alan E. Schiavelli, Reg. No. 32,087; James N. Dresser, Reg. No. 22,973; Carl I. Brundidge, Reg. No. 29,621 and Paul J. Skwierawski, Reg. No. 32,173

Correspondence Address: Direct all communications about the application to:



Customer Number

020457

OR

Type Customer Number here

Place Customer Number Bar
Code Label here

Firm or
Individual Name

Address

Address

City

State

ZIP

Country

Telephone

Fax

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine and imprisonment, or both, under 18 U.S.C. 1001, and that such willful false statements may jeopardize the validity of the application, any patent issuing thereon, or any patent to which this declaration is directed.

I/We hereby claim benefit under Title 35, United States Code, §119 of any provisional application(s) and any foreign application(s) for patent or inventor's certificate listed below and have also identified below any foreign application for patent or inventor's certificate having a filing date before that of the foreign application(s) on which priority is claimed:

Provisional and/or Foreign Application(s)

Priority Claimed

4-320098

(Number)

Japan

(Country)

30 / 11 / 1992

(Day/Month/Year Filed)



Yes



No

4-71116

(Number)

Japan

(Country)

27 / 3 / 1992

(Day/Month/Year Filed)



Yes



No

(Number)

(Country)

(Day/Month/Year Filed)



Yes



No

I/We hereby claim the benefit under Title 35, United States Code, §120 of any United States application(s) listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States application in the manner provided by the first paragraph of Title 35, United States Code, §112, I/we acknowledge the duty to disclose material information as defined in Title 37, Code of Federal Regulations, §1.56(a) which occurred between the filing date of the prior application and the national or PCT international filing date of this application:

(Application Serial No.)

(Filing Date)

(Status: patented, pending, abandoned)

(Application Serial No.)

(Filing Date)

(Status: patented, pending, abandoned)

(Application Serial No.)

(Filing Date)

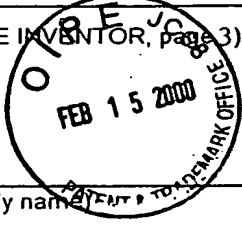
(Status: patented, pending, abandoned)

(Application Serial No.)

(Filing Date)

(Status: patented, pending, abandoned)

Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it displays a valid OMB control number.

(REISSUE APPLICATION DECLARATION BY THE INVENTOR, Page 3)		Docket Number (Optional) 501.32049R00
		
Full name of sole or first inventor (given name, family name) Yujiro KAJIHARA		
Inventor's signature Yujiro Kajihara	Date January 26, 2000	
Residence Same as Post Office Address	Citizenship Japan	
Post Office Address 3-4-8, Minamino, Hachioji-shi, Tokyo, Japan 2-8-15-401, Shibasaki-cho, Tachikawa-shi, Tokyo, Japan Y. K.		
Full name of second joint inventor (given name, family name) Kazunari SUZUKI		
Inventor's signature Kazunari Suzuki	Date January 26, 2000	
Residence Same as Post Office Address	Citizenship Japan	
Post Office Address 4-20-10, Cyuuou, Oota-ku, Tokyo, Japan		
Full name of third joint inventor (given name, family name) Kunihiro TSUBOSAKI		
Inventor's signature Kunihiro Tsubosaki	Date January 29, 2000	
Residence Same as Post Office Address	Citizenship Japan	
Post Office Address 2-29-22, Higashihirayama, Hino-shi, Tokyo, Japan		
Full name of sole or 4th inventor (given name, family name) Hiromichi SUZUKI		
Inventor's signature Hiromichi Suzuki	Date January 26, 2000	
Residence Same as Post Office Address	Citizenship Japan	
Post Office Address 2-19-5-201, Nishiazabu, Minato-ku, Tokyo, Japan		
Full name of 5th joint inventor (given name, family name) Yoshinori MIYAKI		
Inventor's signature Yoshinori Miyaki	Date January 26, 2000	
Residence Same as Post Office Address	Citizenship Japan	
Post Office Address 1-3-17, Wakaba-cho, Tachikawa-shi, Tokyo, Japan		
Full name of 6th joint inventor (given name, family name) Takahiro NAITO		
Inventor's signature Takahiro Naito	Date January 26, 2000	
Residence Same as Post Office Address	Citizenship Japan	
Post Office Address 3-2-40-305, Gakuenhigashi-cho, Kodaira-shi, Tokyo, Japan		
<input checked="" type="checkbox"/> Additional joint inventors are named on separately numbered sheets attached hereto.		

Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it displays a valid OMB control number.

(REISSUE APPLICATION DECLARATION BY THE INVENTOR, page 4)		Docket Number (Optional) 501.32049R00
Full name of sole or 7th inventor (given name, family name) Sueo KAWAI		
Inventor's signature <i>Sueo Kawai</i>		
Residence Same as Post Office Address	Date <i>January 20, 2000</i>	
Post Office Address 1522-49, Ichinoya, Iwama- machi, Nishiibaraki-gun, Ibaraki, Japan	Citizenship Japan	
Full name of 8th joint inventor (given name, family name)		
Inventor's signature		Date
Residence	Citizenship	
Post Office Address		
Full name of 9th joint inventor (given name, family name)		
Inventor's signature		Date
Residence	Citizenship	
Post Office Address		
<input type="checkbox"/> Additional joint inventors are named on separately numbered sheets attached hereto.		

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: KAJIHARA et al.

Application: 09/328,910

(Reissue Application of U.S. Patent 5,637,913; issued June 10, 1997)

Filed: June 9, 1999

For: LEADFRAME SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE
USING THE SAME, AND METHOD OF AND PROCESS FOR
FABRICATING THE TWO

Art Unit: 2811

Examiner: Unassigned (parent application's Examiner: J. Clark)

CERTIFICATE UNDER 37 CFR §3.73

Assistant Commissioner
for Patents
Washington, D.C. 20231

Sir:

Hitachi, Ltd., a corporation located at 6, Kanda Surugadai 4-chome, Chiyoda-ku,
Tokyo, Japan, and Hitachi ULSI Systems Co., Ltd. (Formerly named Hitachi
Microcomputer System Ltd.), a corporation located at 22-1, Josuihoncho 5-chome,
Kodaira-shi, Tokyo, Japan certifies that they are joint assignees
of the entire right, title and interest in the patent identified above by virtue of an
Assignment (copy attached) which has been recorded in the U.S. Patent and Trademark
Office at Reel 7070, Frame 578-579, and by virtue of a Change of Name document
submitted concurrently herewith (under separate cover letter) for recordation (i.e., the
Change of Name pertaining to a change in corporate name from "Hitachi Microcomputer
System Ltd." to "Hitachi ULSI Systems Co., Ltd.")

The undersigned has reviewed all the documents in the chain of title of the patent application identified above and, to the best of undersigned's knowledge and belief, title is in the assignee identified above.

The undersigned (whose title is supplied below) is empowered and authorized to sign this certificate and consent on behalf of the assignee.


As stated also within the Form PTO/SB/53 filed concurrently herewith, the undersigned assignee of the entire interest in the patent identified above, hereby consents to the accompanying reissue application.

I declare that all statements made herein of my own knowledge are true, and that all statements on information and belief are believed to be true, and further state that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under §1001 of Title 18 of the United

States Code and that such willful false statements may jeopardize the validity of the application or any patent issuing thereon.

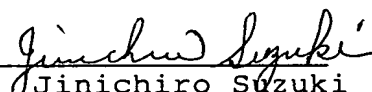
Date: Feb. 10, 2000

Hitachi, Ltd.


Mr. Yasuo Sakuta
Executive Managing Director and
Patent Attorney
Intellectual Property Group
(Authorized Signing Officer)

Date: Feb 8, 2000

Hitachi ULSI Systems Co., Ltd.

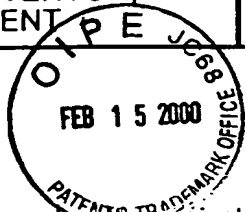

Mr. Jinichiro Suzuki
TITLE: President

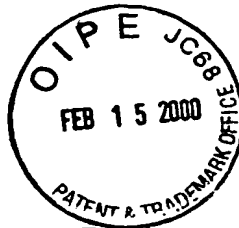
(Authorized Signing Officer)

Attachment:

copy of original Assignment
copy of cover letter and documents regarding
Change of Name

Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it displays a valid OMB control number.

REISSUE APPLICATION BY THE INVENTOR, OFFER TO SURRENDER PATENT		Docket Number (Optional) 501.32049R00	
			
This is part of the application for a reissue patent based on the original patent identified below.			
Name of Patentee(s) Yujiro KAJIHARA, Kazunari SUZUKI, Kunihiro TSUBOSAKI, Hiromichi SUZUKI, Yoshinori MIYAKI, Takahiro NAITO and Sueo KAWAI			
Patent Number 5,637,913		Date Patent Issued June 10, 1997	
Title of invention LEADFRAME SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE USING THE SAME AND METHOD OF AND PROCESS FOR FABRICATING THE TWO			
<p>I am the inventor of the original patent.</p> <p>I offer to surrender the original patent.</p> <p>1. <input checked="" type="checkbox"/> Filed herein is a certificate under 37 CFR 3.73(b).</p> <p>2. <input type="checkbox"/> Ownership of the patent is in the inventor(s), and no assignment of the patent has been made.</p> <p>One of boxes 1 or 2 above must be checked.</p> <p>The written consent of all assignees owning an undivided interest in the original patent is included in this application for reissue.</p>			
Signature A. <u>Yujiro KajiHara</u> B. <u>Kazunari Suzuki</u> C. <u>Kunihiro Tsubosaki</u>		Date A. <u>01/26/2000</u> B. <u>01/26/2000</u> C. <u>01/29/2000</u>	
D. <u>Hiromichi Suzuki</u> E. <u>Yoshinori Miyaki</u> F. <u>Takahiro Naito</u> G. <u>Sueo Kawai</u>		D. <u>01/26/2000</u> E. <u>01/26/2000</u> F. <u>01/26/2000</u> G. <u>Jan. 20, 2000</u>	
Typed or printed name A.) Yujiro KAJIHARA, B.) Kazunari SUZUKI C.) Kunihiro TSUBOSAKI, D.) Hiromichi SUZUKI, E.) Yoshinori MIYAKI, F.) Takahiro NAITO, G.) Sueo KAWAI			
The assignee owning an undivided interest in said original patent is <u>Hitachi, Ltd. and Hitachi ULSI Systems Co., Ltd.</u> , and the assignee consents to the accompanying application for reissue.			
I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under 18 U.S.C. 1001 and that such willful false statements may jeopardize the validity of the application, any patent issued thereon, or any patent to which this declaration is directed.			
Name of assignee Hitachi, Ltd. & Hitachi ULSI Systems Co., Ltd.			
Signature of person signing for assignee I. <u>Yasuo Sakuta</u> II. <u>Jinichiro Suzuki</u>		Date I. <u>Feb. 10, 2000</u> II. <u>Feb. 8, 2000</u> J.S.	
Typed or printed name and title of person signing for assignee Hitachi, Ltd., I. Mr. Yasuo Sakuta, Patent Attorney, Executive Managing Director, Intellectual Property Group (Authorized Signing Officer)			
Typed or printed name and title of person signing for assignee Hitachi ULSI Systems Co., Ltd., II. Mr. Jinichiro Suzuki, President (Authorized Signing Officer)			



Firm Name: Hitachi Microcomputer Engineering Ltd.

Changed to Hitachi Microcomputer System Ltd.
on October 1, 1990
(Date of Registration: October 1, 1990)

Changed to Hitachi ULSI Systems Co., Ltd.
(Date of Registration: April 1, 1998)

Location of Head Office:

1461, Josuihoncho, Kodaira-shi, Tokyo, Japan

1479, Josuihoncho, Kodaira-shi, Tokyo, Japan
(registered on June 10, 1980)

22-1, Josuihoncho 5-chome, Kodaira-shi, Tokyo, Japan
(changed on October 1, 1988 and registered on October 6, 1988)

Public Notice:

given in the official gazette.

Amount of each Share of Par-Value Stock:

five hundred yen (¥500)

Total Number of Shares to be issued:

four hundred thousand (400,000)

eight hundred thousand (800,000)
changed on July 1, 1982 and registered on July 2, 1982

a million six hundred thousand (1,600,000)
changed on April 2, 1984 and registered on April 3, 1984

two million four hundred thousand (2,400,000)
changed on May 27, 1988 and registered on June 7, 1988

three million six hundred thousand (3,600,000)
changed on June 1, 1995 and registered on June 9, 1995

Total Number of Shares issued; and Amount of Capital:

a hundred thousand (100,000);
fifty million yen (¥50,000,000)

a hundred and fifty thousand (150,000);
seventy five million yen (¥75,000,000)
(changed on May 27, 1982 and registered on June 8, 1982)

two hundred thousand (200,000);
a hundred million yen (¥100,000,000)
(changed on July 1, 1982 and registered on July 2, 1982)

four hundred thousand (400,000);
two hundred million yen (¥200,000,000)
(changed on April 1, 1984 and registered on April 3, 1984)

six hundred thousand (600,000);
three hundred million yen (¥300,000,000)
(changed on May 27, 1988 and registered on June 7, 1988)

nine hundred thousand (900,000);
four hundred and fifty million yen (¥450,000,000)
(changed on June 1, 1995 and registered on June 9, 1995)

a million two hundred twenty thousand (1,220,000);
six hundred and ten million yen (¥610,000,000)
(registered on April 1, 1998)

Reason for and Date of Execution of Registration Form:

"Establishment"

Registered on June 2, 1980

Firm Name: Hitachi ULSI Systems Co., Ltd.

Major Business Activities

1. Development, design, manufacturing, inspection, maintenance and sales of semiconductor products and semiconductor system products
2. Development, design, manufacturing, inspection, maintenance, and sales of electronic appliances relating to semiconductor products.
3. Development, design, manufacturing, inspection, maintenance, and sales of software relating to electronic computers and semiconductor products.
4. Engineering business relating to the sales of semiconductor products.
5. Patent research relating to semiconductor electronic appliances.
6. Any business incidental or relating to the preceding items.

Registered on April 1, 1998.

Firm Name: Hitachi ULSI Systems Co., Ltd.

Location of Head Office:

22-1, Josuihoncho 5-chome, Kodaira-shi, Tokyo, Japan

Matters relating to Officers:

Director: Jinichiro Suzuki

Director: Hideki Fukuda

Director: Youzou Tanihara

Director: Takashirou Iwasaki

Director: Seishirou Oowaki

Director: Kunio Hasegawa

Director: Norio Anzai

(These seven persons were reappointed on May 27, 1999 and
were registered on June 11, 1999)

Director: Eiji Takeda

(retired on August 21, 1999 and registered on September 6, 1999)

Representative Director: Jinichiro Suzuki

3-5, Nukui Kita-machi 2-chome,
Koganei-shi, Tokyo, Japan

(these two persons were reappointed on May 27, 1999
and were registered on June 11, 1999)

Auditor: Kiyohiro Shimizu
(installed on April 1, 1998 due to a business merger)

Auditor: Toshio Kiyoshima
(installed on June 21, 1998)

Auditor: Toyoaki Nakamura
(installed on September 6, 1998)

Director: Minoru Shibamiya
(installed on August 21, 1999 and registered on September 6, 1999)
(retired on January 6, 2000 and registered on January 19, 2000)

Director: Yoshihide Tsujihata
(installed on January 6, 2000 and registered on January 19, 2000)

Firm Name: Hitachi Microcomputer Engineering Ltd.

Changed to Hitachi Microcomputer System Ltd.

Changed to Hitachi ULSI Systems Co., Ltd.

Other Matters:

Rules concerning restrictions on the transfer of stock:

Transfer of the stock of this company shall require
the approval of the board of directors.

Hitachi ULSI Engineering Corp. (located at 1-1
Higashi-Koigakubo 3-chome, Kokubunji-shi, Tokyo, Japan)
was merged into Hitachi ULSI Systems Co., Ltd.

Registered on April 1, 1998.

The rules concerning restrictions on the transfer of stock were abolished on April 1, 1998.

Registered on April 1, 1998.

This is a certified copy of the official register.

Date: January 26, 2000

Tanashi Branch of the Tokyo Legal Affairs Bureau

Registrar: Michio Nagano